

AbstractMODULE, SYSTEM AND METHOD FOR TESTING A PHASE LOCKED LOOP

5 (with reference to FIG. 1)

A digital test module (5) is provided for testing a phase locked loop circuit. The module (5) includes phase detection circuitry (10) for performing phase
10 measurements of the phase locked loop circuit and analogue test circuitry (20) for testing at least one analogue element of the phase locked loop circuit. Frequency measurement circuitry (30) is provided for performing frequency measurements of the phase locked
15 loop circuit, as is circuitry (40) for performing calibration and jitter measurements. In this way cycle-to-cycle and phase jitter measurements may be made. A calibration mechanism is provided allowing a process evaluation to be made and which allows the jitter data to
20 be provided in a few seconds. The fully digital design facilitates easy manufacture and ready retargeting of the module to diverse applications and processes.